ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

FINFET WITH LOW GATE CAPACITANCE AND LOW EXTRINSIC RESISTANCE

Application Number: 10/711170

Confirmation Number:

First Named Applicant:

Brent Anderson

Attorney Docket Number:

BUR920040040US1

Art Unit:

2818

Examiner:

HOANG-QUAN HO

Search string:

(6660596 or 6635923 or 6118161 or 20030178670 or 20030151077).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
JAPH	. 1	6660596	2003-12-09	Adkisson, et al		438	286
NAW.	2	6635923	2003-10-21	Hanafi, et al		257	327
	_ 3	6118161	2000-09-12	Chapman, et al		257	401
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US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

1 20030178670 2003-09-25 Fried, et al 257 315 2 20030151077 2003-08-14 Mathew, et al 257 250	init Cite:No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
2 20030151077 2003-08-14 Mathew, et al 257 250	1	20030178670	2003-09-25	Fried, et al		257	315
	2	20030151077	2003-08-14	Mathew, et al		257	250

Signature

Examiner Name	Date
HOANG-QUAN HO	9/15/2005

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HAA		6660596			sson, et al	438	286		$\frac{1}{2}$
HOLA		6635923	2003-10-21	How	nafi, et al	257	+		1
HQA		6118161	2000-09-12	Cha	pman, et al	257	401	·	$\left\{ \right.$
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HOLA		20030178670	2003-09-25	Frio	ed, et al	257	315		4
HOLA		20030151077	2003-08-14	Mat	tew, et al	257	250		$\frac{1}{2}$
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AGH	4	Lindert, et al., "Sub-60 Vol. 22, No. 10, Octobe	l-nm Quasi-Plana r 2001, Pages 48	r FinFE1 7-489	's Fabricated Using a Simp	olified Process	", ILEE Electr	on Device Letters,	
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